# GPU-Accelerated Real Space Electronic Structure Theory on HPC Resources

William P. Huhn<sup>1</sup>, Björn Lange<sup>1</sup>, Victor Yu<sup>1</sup>, Seyong Lee<sup>2</sup>, Mina Yoon<sup>3</sup>, Volker Blum<sup>1</sup> I. Department of Mechanical Engineering and Materials Science, Duke University 2. Computer Science and Math Division, Oak Ridge National Laboratory 3. Center for Nanophase Materials Sciences, Oak Ridge National Laboratory

> MoISSI Workshop/ELSI Conference 2018 August 17, 2018, 2:40 - 3:10 EST



#### Introduction

Rank	System	Cores	(TFlop/s)	(TFlop/s)	(kW)
1	<b>Summit</b> - IBM Power System AC922, IBM POWER9 22C 3.07GHz, NVIDIA Volta GV100, Dual-rail Mellanox EDR Infiniband , IBM DOE/SC/Oak Ridge National Laboratory United States	2,282,544	122,300.0	187,659.3	8,806
2	<b>Sunway TaihuLight</b> - Sunway MPP, Sunway SW26010 260C 1.45GHz, Sunway , NRCPC National Supercomputing Center in Wuxi China	10,649,600	93,014.6	125,435.9	15,371
3	<b>Sierra</b> - IBM Power System S922LC, IBM POWER9 22C 3.1GHz, NVIDIA Volta GV100, Dual-rail Mellanox EDR Infiniband , IBM DOE/NNSA/LLNL United States	1,572,480	71,610.0	119,193.6	
4	<b>Tianhe-2A</b> - TH-IVB-FEP Cluster, Intel Xeon E5-2692v2 12C 2.2GHz, TH Express-2, Matrix-2000 , NUDT National Super Computer Center in Guangzhou China	4,981,760	61,444.5	100,678.7	18,482
5	Al Bridging Cloud Infrastructure (ABCI) - PRIMERGY CX2550 M4, Xeon Gold 6148 20C 2.4GHz, NVIDIA Tesla V100 SXM2, Infiniband EDR , Fujitsu National Institute of Advanced Industrial Science and Technology (AIST) Japan	391,680	19,880.0	32,576.6	1,649
6	<b>Piz Daint</b> - Cray XC50, Xeon E5-2690v3 12C 2.6GHz, Aries interconnect, NVIDIA Tesla P100, Cray Inc. Swiss National Supercomputing Centre (CSCS) Switzerland	361,760	19,590.0	25,326.3	2,272













#### https://www.top500.org/list/2018/06/, accessed 25 June 2018

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#### Introduction

Supporting general-purpose GPU (GPGPU) acceleration is critical for running electronic structure theory on current leading HPC platforms!

## Code targeted: FHI-aims

- All-Electron, Full-Potential KS-DFT
- Localized basis sets: Numeric atomcentered orbitals (NAOs)
- Developed by an active, globallydistributed academic community of 100+ developers



#### Introduction

Supporting general-purpose GPU (GPGPU) acceleration is critical for running electronic structure theory on current leading HPC platforms!



For (non-hybrid) KS-DFT using real-space operations, the rate-limiting steps are:

Integration of the Hamiltonian Matrix Elements

$$H_{mn} = \int \varphi_m^*(\boldsymbol{r}) \hat{h} \varphi_n(\boldsymbol{r}) d\boldsymbol{r}$$

**Electron Density Calculation** 

$$\rho(\boldsymbol{r}) = \sum_{mn} \varphi_m^*(\boldsymbol{r}) D_{mn} \varphi_n(\boldsymbol{r})$$

Solution (or circumvention) of the KS-DFT Equation

$$[\hat{t} + \hat{v}_{ES} + \hat{v}_{XC}] \psi_m(\boldsymbol{r}) = \epsilon \psi_m(\boldsymbol{r})$$

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Solution (or circumvention) of the KS-DFT Equation Calculations  $\begin{bmatrix} \hat{t} + \hat{v}_{ES} + \hat{v}_{XC} \end{bmatrix} \psi_m(\mathbf{r}) = \epsilon \psi_m(\mathbf{r})$  For (non-hybrid) KS-DFT using real-space operations, the rate-limiting steps are:



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Calculations
Calculations
elsi-interchange.org

## Numeric Atom-Centered Orbitals



$$\varphi_{nlm}(\boldsymbol{r}) = \frac{u_n(\boldsymbol{r})}{\boldsymbol{r}} Y_{lm}(\boldsymbol{r})$$

r (log scale)

- Integration points distributed on radial grids around atoms
- Basis elements localized in space give O(N) real-space operation
- Capture core region "wiggles"; naturally all-electron
- Pre-constructed "tiers" of basis elements suitable for DFT:

Basis Set	Н	С	Si
Minimum	ls	ls2s2p	ls2s2p3s3p
Tier I	+sp	+pds	+dpfs
Tier 2	+spsd	+fpsgd	+dgps
•••	•••	• • •	•••

Blum et al., Comp. Phys. Comm. 2009



Becke, J. Phys. Chem 1988; Delley, J.Phys. Chem. 1990; Havu, J Comp. Phys 2009

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As system size grows, number of batches grows linearly

Becke, J. Phys. Chem 1988; Delley, J.Phys. Chem. 1990; Havu, J Comp. Phys 2009

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#### Divide-And-Conquer on the Real-Space Matrix

Each batch only contributes to a small number of real-space matrix elements...



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Hbatch

Each batch only contributes to a small number of real-space matrix elements...

... so for each batch, we only calculate a reduced H<sup>batch</sup> on all basis elements touching that batch

**Basis Functions Touching Batch** 

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Hbatch,max

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Since basis elements are localized, there is an maximum limit on the size of H<sup>batch</sup>!

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Fixed max work done per batch \* O(N<sub>atom</sub>) number of batches = O(N<sub>atom</sub>) overall work

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Two clusters used:

- timewarp: Development cluster of aims group at Duke University
- PSG: Benchmarking cluster for Tesla GPGPUs at NVIDIA
- Three types of nodes used:

Node	Ivy Bridge/GP100	Haswell/P100	Skylake/V100		
Cluster	timewarp	PSG	PSG		
	2x Intel Xeon E5-2670v2	2x Intel Xeon E5-2698v3	2x Intel Xeon Gold 6148		
Cro	(20 cores*, Ivy Bridge)	(32 cores, Haswell)	(20 cores, Skylake)		
GPGPU**	Ix Quadro GP100	4x Tesla P100	4x Tesla V I 00		
	(Pascal)	(Pascal)	(Volta)		
MPI Tasks/GPGPUs	16/1	32/4	20/4		
Compilers/Libraries	ifort 14.0, MKL 11.1.1, IMPI 4.1.3, CUDA 8.0	ifort 17.0, MKL 11.3.3, IMPI 5.0.3, CUDA 9.1	ifort 17.0, MKL 11.3.3, IMPI 5.0.3, CUDA 9.1		
<ul> <li>* Due to usage of MPS, only 16 MPI tasks used for Ivy Bridge/GP100</li> <li>** All GPGPUs are PCI-E models</li> </ul>					

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# A Complete All-Electron Calculation: Si (3x3x3)



We perform a complete all-electron calculation including forces and analytical stress tensor for a 3x3x3 supercell of Si

3x3x3 supercell (54 atoms)

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# A Complete All-Electron Calculation: Si (3x3x3)



We perform a complete all-electron calculation including forces and analytical stress tensor for a 3x3x3 supercell of Si

This calculation is representative of a single iteration of a geometric optimization calculation, containing

- Initialization (not GPGPU accelerated)
- I2 "Normal" SCF Iterations
- I SCF + Forces + Analytical Stress Tensor Iteration

Diamond Si 3x3x3 supercell (54 atoms)

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This calculation is representative of a single iteration of a geometric optimization calculation, containing

- Initialization (not GPGPU accelerated)
- 12 "Normal" SCF Iterations
- I SCF + Forces + Analytical Stress Tensor Iteration

Computational Details:

- PBE functional
- Tight integration settings
- Tight basis sets
- SCF k-grid: IxIxI
- Load balancing for GPGPU, CSR for CPU



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#### Back to PBE...

### Speedups for Diamond Si (3x3x3)

	Ivy Bridge/GP100	Haswell/P100	Skylake/V100
SCF Iteration	2.9x	2.5x	3.1x
SCF Iteration + Forces	4.4x	5.8x	5.7x
SCF Iteration + Forces + AS	5.2x	6.7x	7.8x
Entire Calculation	3.8x	3.7x	<b>4.0</b> x

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#### Do these results generalize to other materials?

## **103 Compound Material Benchmark Set**

#### To systematically verify the performance of the GPGPU code, we use the 103 compound material test set from Huhn and Blum:

PHYSICAL REVIEW MATERIALS 1, 033803 (2017)	Family	No. materials	Materials	
One-hundred-three compound band-structure benchmark of post-self-consistent spin-orbit coupling treatments in density functional theory	Elementals	45	$\underline{\text{Be, C}} [\text{GRA}], \text{Ne, } \underline{\text{Mg}},$ Al, Si, Ca, Sc, Ti, V, Cr, Mp, Eq. Co, Ni	
William P. Huhn <sup>*</sup> and Volker Blum <sup>†</sup>			Cu, Zn, Ge, Sr, Y,	
(Received 4 May 2017; published 30 August 2017)			Zr, Nb, Mo, Tc, Ru,	
			Rh, Pd, Ag, <u>Cd</u> , Sn,	
			Xe, Ba, Lu, Hf, Ta,	
3x3x3 supercells of the primitive cell for a			W, Re, Os, II, Pt, Au Tl Ph Bi Po	
given material was used vielding unit cell	Compound	37	C [DIA], MgO, AlN [WUR],	
given material was used, yielding unit cen	semiconductors		AlN [ZB], SiC, BP, AlP,	
sizes ranging from 27, 54, and 108 atoms			MgS, ZnO, ZnS [WUR],	
			ZnS [ZB], GaN [WUK], GaN [ZB] GaP AlAs	
			BAs, GaAs, MgSe, ZnSe,	
Computational Dataila			CdS [WUR], CdS [ZB],	
Computational Details.			CdSe [WUR], CdSe [ZB],	
PBF functional			InN, InP, InAs, AlSb, CaSh, InSh, ZaTa, CdTa	
			HgS. HgSe. HgTe.	
<ul> <li>Tight integration settings</li> </ul>			PbS, PbSe, PbTe	
	Alkali	21	LiF, NaF, LiCl, NaCl,	
• I Ight basis sets	halides		KF, KCl, LiBr, NaBr,	
• SCE k-grid. IVIVI			Lil Nal KI Rbi	
			CsF, CsCl [CSCL],	
<ul> <li>Load balancing was used (critical!)</li> </ul>			CsCl [RS], CsBr, CsI	



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## SCF Iteration + Forces + Stress for 103 Compounds: Skylake/V100



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## TL; DR

### Speedups for Diamond Si (3x3x3)

	Ivy Bridge/GP100	Haswell/P100	Skylake/V100	
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Speedups for 103 Compound Test Set				
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## Strong Scaling for GPGPU Calculations: 375 Atom Bi<sub>2</sub>Se<sub>3</sub> Slab



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#### SCF Iteration + Forces + Stress on Titan: 375 Atom Bi<sub>2</sub>Se<sub>3</sub> Slab



#### SCF Iteration + Forces + Stress on Titan: 375 Atom Bi<sub>2</sub>Se<sub>3</sub> Slab



All-electron real-space DFT can be effectively GPGPU-accelerated, with ideal scaling on HPC resources, using domain decomposition

For realistic (non-hybrid) calculations with geometry relaxation, can expect  $\approx 3x-4x$  speedup with FHI-aims on modern architectures

GPGPU acceleration of real-space operations shown in this talk available on mainline FHI-aims git repo, production-ready

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